REMARKS

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Claim 66 has been amended.

Claims 1-66 are pending.

Rejections under 35. U.S.C. §103(a)

Claims 1-66 stand rejected under 35 U.S.C.§103(a) as being unpatentable over U.S. Patent No. 4,835,682, issued to Kurachi et al. (hereinafter referred to as *Kurachi et al.*) in view of U.S. Patent No. 6,343,280, issued to Clark (hereinafter referred to as *Clark*) further in view of U.S. Patent No. 5,891,761, issued to Kean (hereinafter referred to as *Kean*).

Applicants respectfully traverse these rejections for at least the following reasons.

Kurachi et al. disclose techniques for preventing unauthorized copying of software programs stored on floppy drives. To do this, Kurachi et al. take advantage of a floppy disk controller/drive that supports two different frequency modulation modes, namely a standard frequency modulation (FM) mode and a non-standard frequency modulation mode (MFM). Kurachi et al. also utilize a machine ID associated with the host computer that is attempting to load a program that is stored on a floppy disk using the MFM mode. In order to read the floppy disk, the host computer is required and configured to convert the MFM written program and rewrite it to the floppy disk using the FM mode. During the rewrite to the floppy disk, the program is modified based on the machine ID such that when the floppy disk is read in the future only the host computer will be allowed to run the modified program stored on the floppy disk. Kurachi et al. teach that the modified program is de-modified and the resulting original program is then

allowed to run. If the rewritten floppy disk is subsequently inserted into another computer having a different machine ID, the program cannot be de-modified and therefore cannot be run.

Clark teaches that keys can be distributed by a software vender to a software user and a remote licensing agent, and that the remote licensing agent can be configured to execute certain instructions for the software user provided the validity of an appropriate license. This is a form of distributed processing.

Kean discloses techniques by which field programmable gate arrays (FPGAs) are configured into a plurality of cells having hardware based logic elements that can be connected together.

Independent Claim 1 is directed towards a method that includes providing an initial digital good to at least one computer. The initial digital good includes a plurality of selectively arranged parts in an initial configuration and the initial digital good is configured as to not properly function with the computer. The method further includes, with the at least one computer, receiving unique key data, and converting the initial digital good into a modified digital good using the unique key data to selectively individualize the initial digital good for use with the computer, such that the plurality of selectively arranged parts in the modified digital good have been rearranged to have a substantially unique operative configuration that properly functions with the computer and is different than the initial configuration. The method also includes causing the computer to run the modified digital good.

Neither Kurachi et al., Clark and/or Kean disclose or otherwise reasonably suggest taking a digital good (or portion thereof) having a plurality of selectively arranged parts in an initial configuration and rearranging such parts in a different

configuration to produce a modified digital good (or portion thereof). Here, for example, unique key data can be used in the process of selectively rearranging the plurality of parts.

Kurachi et al. teach that a digital good can be modified on a floppy disk using a floppy disk controller/drive that supports two different frequency modulation modes, namely a standard frequency modulation (FM) mode and a non-standard frequency modulation mode (MFM). There is no rearrangement of parts of a digital good. In their distributed processing system, Clark does not teach that selectively arranged parts of a digital good (or portion thereof) are rearranged. *Kean*, which is alleged to disclose that an initial digital good can include a plurality of selectively arranged parts in an initial condition, teaches that in CAL II FPGA design a plurality of replaceable blocks of static logic design and similar signal interfaces can be designed and provided in a runtime library and alternative FPGA configurations considered/tested in deciding with subset of replaceable blocks to implement. The so-called digital good in *Kean* is not actually a modified digital good that has be rearranged to have a substantially unique operative configuration that properly functions with the computer and is different than the initial configuration. Instead, the resulting rearranged circuit layout represents a chosen design that only properly functions when burned into an FPGA. As far as the digital good aspect of *Kean's* invention is concerned, the CAL II or CAD design software that selects circuit layout design blocks from the library does not get rearranged itself, not does the runtime library or block therein get rearranged in any manner.

Simply stated, Kean does not produce a digital good analogous to the modified digital good as recited in the pending claims. Consequently, and in

additional there appears no reasonable motivation for one skilled in the art to even begin to combine the disparate teachings of Kean with *Kurachi et al.* and/or *Clark*.

For at least these reasons, Claim 1 is clearly patentable over the cited art and in condition for prompt allowance, as are Claims 2-17 which depend there from and add further limitations thereto.

Independent Claim 18 is directed towards a computer-readable medium comprising computer-executable instructions for, with the at least one computer, receiving an initial digital good, wherein the initial digital good includes a plurality of selectively arranged parts in an initial configuration and the initial digital good is configured as to not properly function with the computer, receiving unique key data, and converting the initial digital good into a modified digital good using the unique key data to selectively individualize the initial digital good for use with the at least one computer, such that the plurality of selectively arranged parts in the modified digital good are rearranged to have a substantially unique operative configuration that properly functions with the at least one computer and is different than the initial configuration.

For at least the same reasons stated above with regard to the method of Claim 1, Claim 18 is also clearly patentable over the cited art and in condition for prompt allowance, as are Claims 19-26 which depend there from and add further limitations thereto.

Independent Claim 27 is directed towards a computer-readable medium comprising computer-executable instructions for receiving unique identifier data associated with at least one computer, generating unique key data based on at least the unique identifier data, receiving at least a portion of an initial digital good having a plurality of selectively arranged parts in an initial configuration,

converting the at least a portion using the unique key data to selectively individualize the portion, such that a modified portion of the digital good is produced having the plurality of parts rearranged in a different configuration than the initial configuration, and providing at least the modified portion of the digital good and at least a portion of the unique key data to the at least one computer.

Again for at least the reasons stated above with regard to the method of Claim 1, the computer-readable medium of Claim 27 is also clearly patentable over the cited art and in condition for prompt allowance, as are Claims 28-33 which depend there from and add further limitations thereto.

Independent Claim 34 is drawn to an apparatus for use in a host computer. The recited apparatus includes an individualizer that is configured to receive unique key data and at least a portion of an initial digital good that includes a plurality of selectively arranged parts in an initial configuration, and produce at least a portion of a modified digital good using the unique key data to selectively individualize the initial digital good for use with the host computer, and such that the plurality of selectively arranged parts in the modified digital good are rearranged to be operatively different in configuration than the initial configuration of the digital good.

Again for at least the reasons stated above with regard to the method of Claim 1, the apparatus of Claim 34 is also clearly patentable over the cited art and in condition for prompt allowance, as are Claims 35-42 which depend there from and add further limitations thereto.

Independent Claim 43 is directed towards an apparatus for use in a source computer. Here, the recited apparatus includes a key generator configured to receive a unique identifier data from a destination computer and generate unique



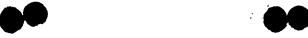


key data based on the received unique identifier data associated with the destination computer. The apparatus also includes an individualizer configured to receive the unique key data and at least a portion of an initial digital good having a plurality of selectively arranged parts in an initial configuration and output at least a portion of a modified digital good using the unique key data to selectively individualize the initial digital good, such that in the modified digital good the plurality of selectively arranged parts have been rearranged to have an operatively different configuration than the initial configuration.

For at least the reasons stated above with regard to the method of Claim 1, the apparatus of Claim 43 is also clearly patentable over the cited art and in condition for prompt allowance, as are Claims 44-49 which depend there from and add further limitations thereto.

Independent Claim 50 is drawn to a system that includes an identifier configured to output unique identifier data associated with a computer, a key generator coupled to receive the unique identifier data and generate at least one unique key data based on the received unique identifier data, and at least one individualizer configured to receive the unique key data and at least a portion of an initial digital good that includes a plurality of selectively arranged parts in an initial configuration, and output at least a portion of a modified digital good using the unique key data to selectively individualize the initial digital good, such that the plurality of selectively arranged parts in the modified digital good have been rearranged to be operatively different in configuration than the initial configuration of the digital good.

For at least the reasons stated above with regard to the method of Claim 1, the system of Claim 50 is also clearly patentable over the cited art and in condition



for prompt allowance, as are Claims 51-66 which depend there from and add further limitations thereto.

Conclusion

Date: 9-18-2003

For at least these substantial reasons, it is respectfully requested that all of the rejections be reconsidered and withdrawn. The pending claims have been placed in condition for allowance and are clearly patentable over the cited art and should therefore be allowed.

Respectfully Submitted,

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